

Notice of References Cited	Application/Control No. 10/710,001	Applicant(s)/Patent Under Reexamination MO ET AL.	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,653,201	11-2003	Chung, Yi Sun	438/424
	B	US-6,798,038	09-2004	Sato et al.	257/510
	C	US-6,713,385	03-2004	Pipes et al.	438/637
	D	US-5,902,127	05-1999	Park, Tai-su	438/435
	E	US-6,531,377	03-2003	Knorr et al.	438/435
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. II, pgs. 66-67
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.